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FORM**

(to be used for all correspondence after initial filing)

Total Number Of Pages In This Submission

6

Application Number

09/936,032

Filing Date

September 7, 2001

First Named Inventor

Michael NICOLAIDIS

Examiner Name

To Be Assigned

Group Art Unit

To Be Assigned

Attorney Docket No.

514842000100

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**ENCLOSURES (check all that apply)**☐ Fee Transmittal Form☐ Fee Attached☐ Amendment☐ After Final☐ Affidavits/declarations☐ Extension of Time Request☐ Express Abandonment Request☐ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Response to Missing Parts/  
Incomplete Application☐ Response to Missing Parts  
under 37 CFR 1.52 or 1.53☐ Assignment Papers☐ Drawing(s)☐ Licensing-related Papers☐ Petition☐ Petition to Convert to a  
Provisional Application☒ Power of Attorney (3 pages)  
Revocation/Change of  
Correspondence Address☐ Terminal Disclaimer☐ Request for Refund☐ CD, Number of CD(s) \_\_\_\_\_☐ After Allowance Communication to  
Group☐ Appeal Communication to Board of  
Appeals and Interferences☐ Appeal Communication to Group  
(Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☒ Other Enclosures (please identify below):1. Statement Under 37 C.F.R. 3.73(b)  
w/copy of Assignment (2 pages)

2. Return Receipt Postcard

Remarks

**SIGNATURE OF APPLICANT, ATTORNEY OR AGENT**

Firm

or

Individual Name

Morrison &amp; Foerster, 425 Market Street, San Francisco, CA 94105

Erwin J. Basinski, Reg. No. 34,773

Signature

Date

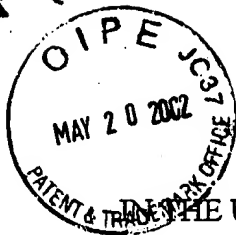
May 3, 2002

**CERTIFICATE OF MAILING BY "FIRST CLASS MAIL"**

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Valerie L. Cohen

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#10/8-902  
U. Jones

PATENT  
Docket No. 514842000100

In the application of:

Michael NICOLAIDIS

Serial No.: 09/936,032

Filing Date: September 7, 2001

For: LOGIC CIRCUIT PROTECTED  
AGAINST TRANSIENT  
DISTURBANCES

Examiner: To Be Assigned

Group Art Unit: To Be Assigned

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PROSECUTION BY ASSIGNEE AND POWER OF ATTORNEY  
UNDER 37 C.F.R. § 3.71

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

iRoC Technologies, the assignee of the entire right, title and interest in this patent application, under 37 C.F.R. § 3.71 hereby appoints:

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Mehran Arjomand (Reg No. 48,231)	Laurie A. Axford (Reg No. 35,053)
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all of Morrison & Foerster LLP, 425 Market Street, San Francisco, California 94105-2482, telephone (415) 268-7000, to prosecute this application and transact all matters in the United States Patent and Trademark Office connected therewith, said appointment to be to the exclusion of the inventors and their attorneys in accordance with the provisions of 37 C.F.R. § 3.71 provided that if any one of said attorneys or agents ceases being affiliated with the law firm of Morrison & Foerster LLP as partner, employee or of counsel, such attorney's or agent's appointment as attorney or agent and all powers derived therefrom shall terminate on the date such attorney or agent ceases being so affiliated.

Please direct all written communications relative to this application to:

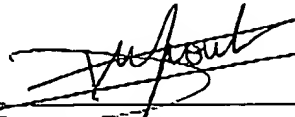
Erwin J. Basinski  
Morrison & Foerster LLP  
425 Market Street  
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Please direct all telephone communications to Erwin Basinski at (415) 268-7144.

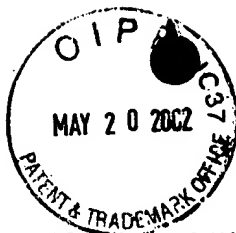
iRoC Technologies  
a French corporation

Dated: April 15, 2002

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PTO/SB/96 (08-00)  
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**STATEMENT UNDER 37 CFR 3.73(b)**

Applicant/Patent Owner: Michaël NICOLAIDIS

Application No./Patent No.: 09/936,032

Filed/Issue Date: September 7, 2001

Entitled: **LOGIC CIRCUIT PROTECTED AGAINST TRANSIENT DISTURBANCES**

iRoC Technologies, a corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.  
The extent (by, percentage) of its ownership interest is \_\_\_\_\_ %

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: \_\_\_\_\_ To: \_\_\_\_\_  
The document was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.
2. From: \_\_\_\_\_ To: \_\_\_\_\_  
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3. From: \_\_\_\_\_ To: \_\_\_\_\_  
The document was recorded in the United States Patent and Trademark Office at Reel \_\_\_\_\_, Frame \_\_\_\_\_, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

☒ Copies of assignments or other documents in the chain of title are attached.

**[NOTE]:** A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

April 15, 2002  
**iRoC Technologies, SA**  
WTC, BP 1510  
38025 Grenoble Cedex 01  
428 924 732 RCS Grenoble  
Tél. : 33 4 38 12 07 63  
Fax : 33 4 38 12 96 15  
e-mail: info@iroctech.com

Eric DUPONT  
[Signature]  
Signature  
P-DG / CEO  
Title

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sf-1272125

Attorney Docket No.: 514842000100

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ASSIGNMENT

In consideration of the premises and other good and valuable consideration in hand paid, the receipt and sufficiency of which is hereby acknowledged, the undersigned,

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(1) NICOLAIDIS Michael  
(2) \_\_\_\_\_  
(3) \_\_\_\_\_

(4) \_\_\_\_\_  
(5) \_\_\_\_\_  
(6) \_\_\_\_\_

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who have made a certain new and useful invention, hereby sell, assign and transfer unto

iRoC Technologies  
World Trade Center  
B.P. 1510  
38025 GRENOBLE CEDEX 1, FRANCE

its successors and assign (hereinafter designated "ASSIGNEE") the entire right, title and interest for the United States of America as defined in 35 U.S.C. 100 in the invention entitled

LOGIC CIRCUIT PROTECTED AGAINST TRANSIENT DISTURBANCES

(a) for which an application for United States Letters Patent was filed on September 7, 2001, and identified by United States Serial N° 09/936,032; or

(b) for which an application for United States Letters Patent was executed on \_\_\_\_\_,

and the undersigned hereby authorize and request the United States Commissioner of Patents and Trademarks to issue any and all United States Letters Patent which may be granted therefor and any and all extensions, divisions, reissues, substitutes, renewals or continuations thereof, and the right to all benefits under the International Convention for the Protection of Industrial Property to the said ASSIGNEE, for its interest as ASSIGNEE, its successors, assigns and legal representatives; the undersigned agree that the attorneys of record in said application shall hereafter act on behalf of said ASSIGNEE;

AND the undersigned hereby agree to transfer a like interest, and to render all necessary assistance in making application for and obtaining original, divisional, reissued or extended Letters Patent of the United States; upon request of the said ASSIGNEE, its successors, assigns and legal representatives, and without further remuneration, in and to any improvements, and applications for patent based thereon, growing out of or related to the said invention; and to execute any papers by the said ASSIGNEE, its successors, assigns and legal representatives, deemed essential to ASSIGNEE's full protection and title in and to the invention hereby transferred.

SIGNED on the dates indicated aside our signatures:

INVENTORS

1) NICOLAIDIS Michael  
Name: \_\_\_\_\_  
2) \_\_\_\_\_  
Name: \_\_\_\_\_  
3) \_\_\_\_\_  
Name: \_\_\_\_\_  
4) \_\_\_\_\_  
Name: \_\_\_\_\_  
5) \_\_\_\_\_  
Name: \_\_\_\_\_  
6) \_\_\_\_\_  
Name: \_\_\_\_\_

DATE SIGNED

28-2-2002  
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